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## Patent Abstracts of Japan

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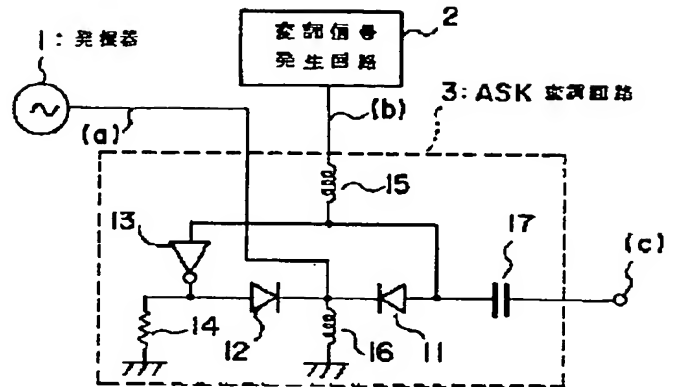
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TITLE : ASK MODULATION CIRCUIT



**ABSTRACT :** PROBLEM TO BE SOLVED: To set input impedance at the time of modulation and at the time of non-modulation to be constant with simple constitution and to relieve the drop of switching speed on an off-side by providing a terminal resistance having the same resistance value as the input impedance of a load and connecting a carrier to the terminal resistance at the time of non-modulation.

**SOLUTION:** The resistance value of the terminal resistance 14 is previously set to be the same value as the impedance of the load. When a modulation signal (b) from a modulation signal generation circuit 2 becomes 'H', forward current flows in a PIN diode 11 and the carrier (a) from an oscillator 1 is outputted as a modulation wave (c). The impedance of an ASK modulation circuit 3, which is viewed from the oscillator 1 at that time, becomes the input impedance of the load. When the modulation signal (b) becomes 'L', the output of an inverter 13 becomes 'H' and forward current flows in a PIN diode 12 and the carrier (a) from the oscillator 1 flows to the terminal resistance 14. Thus, the impedance of the ASK modulation circuit 3, which is viewed from the oscillator, can be made constant and the output frequency of the oscillator 1 can be stabilized.

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